

REMARKS

Reconsideration of this application is respectfully requested in view of the foregoing amendment and the following remarks.

Claims 1-12 were pending in this application. Claims 1 and 7 have been amended hereby. Accordingly, claims 1-12 will be pending herein upon entry of this Amendment. For the reasons stated below, Applicant respectfully submits that all claims pending in this application are in condition for allowance.

In the Office Action, claims 1-12 were rejected under 35 U.S.C. §112, first and second paragraphs. Claims 1-12 were also rejected under 35 U.S.C. §102(b) as being anticipated by Peterson et al., or under 35 U.S.C. §103(a) as being unpatentable over Peterson et al. in view of Kwon et al. or Obie et al. in view of Olmstead et al., Kwon et al., and Kanagawa et al. These grounds of rejection are respectfully traversed.

As to the §112 rejections, the paragraph bridging pages 6 and 7 expressly states “[t]he variable resistor R_f is utilized to provide different resistances upon the display mode and required bandwidth.” In other words, the circuitry depicted and described in the present application is designed to operate on input signals that are consistent with different display modes that are well known by those skilled in the art, such as VGA, SVGA, XGA, SXGA. The claim language states that the analog image signal has a display mode (“with a display mode”), and that the filter is responsive to that display mode (“bandwidth adjustable in response to said display mode”). Applicant submits that this language is well-supported by the specification and

would be well understood by one of ordinary skill in the art. For these reasons, Applicant respectfully requests that the §112 rejections be reconsidered and withdrawn

With respect to the art-based claim rejections, the Examiner is directed to Figures 2 and 3 of the application. As shown, the low pass filter 24 comprises a variable resistor R_f connected between the input node 20 and an internal node 28 and a capacitor C_f connected between the internal node 28 and a ground node. The variable resistor R_f is utilized to provide different resistances upon the display mode and required bandwidth. As an example, the resistance provided for the VGA mode of a 640x480 active resolution should be greater than that for XGA mode of a 1024x768 active resolution. The resistance of the variable resistor R_f can be changed by a control signal that is generated in response to the display mode of the analog image signal. The low pass filter 24 is used to remove high-frequency noise from the image signal V_{in} with a display mode, such as VGA, SVGA, XGA, SXGA and so on. The relevant description is provided in the paragraph bridging pages 6-7 of the specification. Claim 1 and 7 have been amended to recite that "said filter provides a bandwidth adjustable in response to said display mode."

In contrast, Peterson et al. (US Patent No. 5,926,217) disclose in Figure 3 a *predetermined bias voltage* V_{DET} (see col. 4, lines 6-10) that is not an analog image signal, and discloses "[t]he bandwidth of the low pass filter 117 *must be controlled, so the amount of current passing through the diode D2 is of concern* (see col. 6, lines 40-46) which is not adjustable in response to any display mode. In addition, none of Kwon et al. (US Patent No. 6,724,245), Olmstead et al. (US Patent Number 5,814,803), Obie et al. (US Patent Number

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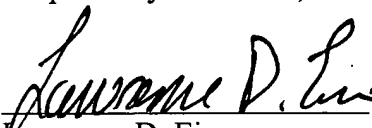
5,038,096) and Kanagawa et al. (US Patent No. 6,366,866) discloses that a "*filter provides a bandwidth adjustable in response to said display mode*" as recited in the currently amended claims.

For the reasons stated above, Applicant respectfully requests that the §102 and §103 rejection be reconsidered and withdrawn.

In view of the foregoing all of the claims in this case are believed to be in condition for allowance. Should the Examiner have any questions or determine that any further action is desirable to place this application in even better condition for issue, the Examiner is encouraged to telephone applicant's undersigned representative at the number listed below.

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